

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An electronic ~~circuit~~circuit, comprising:  
~~an electronic element;~~  
a capacitor ~~to accumulate a data~~that is capable of accumulating a current signal  
and a voltage signal in a form of an amount of charge; and  
a first transistor whose conduction state is set in accordance with the amount of charge accumulated in the capacitor, the first transistor including a first gate, a first drain and a first source, the first transistor supplying ~~an amount of current~~a current whose amount is determined in accordance with the conduction state to ~~the~~an electronic ~~element~~element.  
~~the capacitor being capable of accumulating a data current and a data voltage as the data signal.~~
2. (Currently Amended) The electronic circuit according to Claim 1, further comprising:  
~~the data current being a multi value data current,~~a second transistor,  
~~the data voltage being a binary data voltage, and~~  
the ~~multi value data current~~ signal and the ~~binary data voltage~~ signal being supplied to the capacitor ~~via a~~through the second transistor.
3. (Currently Amended) The electronic circuit according to Claim 1, further comprising:  
a third transistor ~~being provided~~that controls an electrical connection between ~~at the first gate and a drain of the first transistor~~drain.
4. (Currently Amended) The electronic circuit according to Claim 1,  
~~further comprising:~~

a fourth transistor to ~~determine~~that controls a timing to start or stop supply of the current to the electronic element after the conduction state of the first transistor is set according to ~~the data signal~~at least one of the current signal and the voltage signal.

5. (Currently Amended) ~~An~~The electronic circuit, circuit according to Claim 1, further comprising:

~~\_\_\_\_\_~~an electronic element;

~~\_\_\_\_\_~~a capacitor that is capable of accumulating a data current and a data voltage as a data signal in a form of an amount of charge;

~~\_\_\_\_\_~~a first transistor whose conduction state is set in accordance with the amount of charge accumulated in the capacitor, the first transistor supplying an amount of current in accordance with the conduction state to the electronic element; and

~~a fifth transistor to reset~~transistor, the amount of charge held in the capacitor being reset to a predetermined state when the fifth transistor is turned on.

6. (Currently Amended) ~~An electro-optical device including~~device, comprising:

~~\_\_\_\_\_~~a plurality of scanning lines, lines;

~~\_\_\_\_\_~~a plurality of data lines, and lines;

~~\_\_\_\_\_~~a plurality of unit circuits, the electro-optical device comprising: circuits;

~~a data voltage outputting~~first circuit that outputs ~~binary data voltages to a~~current signal that is accumulated in a capacitor included in each of the plurality of unit circuits ~~via the plurality of data lines; circuits; and~~

~~a data current outputting~~second circuit that outputs ~~data currents to a~~voltage signal that is accumulated in a capacitor in each of the plurality of unit circuits ~~via the plurality of data lines.~~

7. (Currently Amended) The electro-optical device according to Claim 6,  
the ~~data voltages and the data currents~~current signal and voltage signal being  
supplied ~~via~~to each of the plurality of unit circuits through one data line of the plurality of  
data lines.
8. (Currently Amended) The electro-optical device according to Claim 6,  
the plurality of data lines including a plurality of first data lines and a plurality  
of second data lines,  
the current signal being supplied to each of the plurality of unit circuits  
through one first data line of the plurality of first data lines; and  
the ~~data voltages and the data currents~~voltage signal being supplied ~~via~~  
~~different data lines of~~to each of the plurality of unit circuits through one second data line of  
the plurality of second data lines, respectively.
- 9-12. (Canceled)
13. (Currently Amended) The electro-optical device according to Claim 6;22,  
the electro-optical ~~elements~~element being ~~EL elements~~an EL element.
14. (Currently Amended) The electro-optical device according to Claim 13,  
each ~~of the EL elements~~having element including a light-emitting layer that is  
composed of an organic material.
- 15-19. (Canceled)
20. (Previously Presented) An electronic apparatus, comprising:  
the electro-optical device according to Claim 6.
21. (New) The electronic circuit according to Claim 1,  
the current signal being a multi-valued data current, and  
the voltage signal being a binary data voltage.

22. (New) The electro-optical device according to Claim 6,  
each of the plurality of unit circuits including an electro-optical element.
23. (New) An electronic circuit, comprising:  
a capacitor that accumulates a current signal during a first period, the capacitor  
accumulating a voltage signal during a second period; and  
a first transistor whose conduction state is set in accordance with an amount of  
charge accumulated in the capacitor stored during a selected period from the first period and  
the second period, the first transistor including a first gate, a first drain and a first source, the  
first transistor supplying a current whose amount is determined in accordance with the  
conduction state to an electronic element.
24. (New) An electronic circuit, comprising:  
a capacitor that accumulates a current signal in a first mode, the capacitor  
accumulating a voltage signal in a second mode; and  
a first transistor whose conduction state is set in accordance with an amount of  
charge accumulated in the capacitor stored during a selected mode from the first mode and  
the second mode, the first transistor including a first gate, a first drain and a first source, the  
first transistor supplying a current whose amount is determined in accordance with the  
conduction state to an electronic element.
25. (New) The electronic circuit according to Claim 23,  
the current signal corresponding to analog data, and  
the voltage signal corresponding to digital data.
26. (New) The electronic circuit according to Claim 24,  
the current signal corresponding to analog data, and  
the voltage signal corresponding to digital data.
27. (New) The electronic circuit according to Claim 24,

a power consumption in the second mode being lower than a power consumption in the first mode.

28. (New) The electronic circuit according to Claim 23, further comprising:  
a second transistor,  
the current signal and the voltage signal being supplied to capacitor through the second transistor.

29. (New) The electronic circuit according to Claim 23, further comprising:  
a third transistor that controls an electrical connection between the first gate and the first drain.